

## IN THE SPECIFICATION

Amend the specification as follows:

Page 5, between lines 14 and 15, insert the following:

figures 5 and 6 illustrate further embodiments of an inverter charging FET whose gate electrode is capacitively coupled to one of the FET's source/drain electrodes.

Page 7, line 36, add the following:

Figs. 5 and 6 each illustrate a different embodiment of an inverter with a charging FET transistor 4 having a gate electrode 30 and source/drain electrodes 8 and 32 and a respective coupling capacitor 14 or 16. In Fig. 5, gate electrode 30 of transistor 4, electrode 34 of transistor 2, one terminal of capacitor 14 and inverter output 12 are coupled to junction 36. The other terminal of capacitor 14 is coupled to the transistor gate electrode 30. Thus the gate electrode 30 of FET transistor 4 is coupled to one of the transistor's source/drain electrodes 8/32 solely by a capacitor, e.g., capacitor 14.

In Fig. 6, electrode 32 of transistor 4, electrode 34 of transistor 2 and output 12 are coupled to junction 38. One terminal of capacitor 16 is coupled to the gate electrode 30 and the other terminal of capacitor 16 is coupled to one of the transistor source/drain electrodes 8/34. Thus the gate electrode 30 of FET transistor 4 is coupled to one of the transistor's source/drain electrodes 8/34 solely by a capacitor, e.g., capacitor 16.